



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Masataka HOSHINO

Group Art Unit: **2823**

Serial No.: **09/982,963**

Examiner: **COLEMAN, William D.**

Filed: **October 22, 2001**

P.T.O. Confirmation No.: **8557**

For: **THREE DIMENSIONAL SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
HAVING A PIERCING ELECTRODE**

RESPONSE UNDER 37 C.F.R. §1.116
- EXPEDITED RESPONSE -

Mailstop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

October 24, 2003

Sir:

In response to the Office Action dated July 11, 2003, reconsideration of the rejections in view of the following remarks is respectfully requested.

Remarks/Arguments begin on page 2 of this paper.

RECEIVED
OCT 29 2003
TECHNOLOGY CENTER 2800